

Notice of References Cited	Application/Control No. 10/064,333	Applicant(s)/Patent Under Reexamination GREENBLAT ET AL.	
	Examiner Derrick W. Ferris	Art Unit 2663	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,886,992 A	03-1999	Raatikainen et al.	370/410
	B	US-6,687,757 B1	02-2004	Ben-Ze'ev et al.	709/250
	C	US-5,822,568 A	10-1998	Swanstrom, Scott	703/24
	D	US-2002/0191601 A1	12-2002	Cook, B. Scott	370/389
	E	US-6,496,516 B1	12-2002	Dabecki et al.	370/460
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	DUNLOP et al. "Alternative approach to ASIC design methodology based on reconfigurable logic devices". IEEE. April 1992. Pages 217-221.			
	V				
	W				
	X				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.